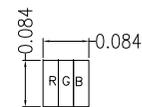


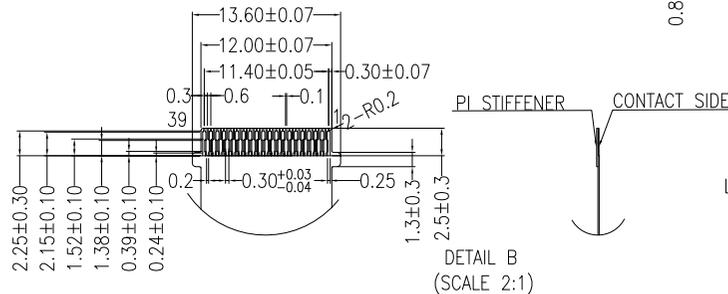
PIN ASSIGNMENT			
PIN	SYMBOL	PIN	SYMBOL
1	GND	21	IOVCC
2	D2P	22	GND
3	D2N	23	NC
4	GND	24	AVEE
5	D1P	25	AVDD
6	D1N	26	NC
7	GND	27	LEDK
8	CLKP	28	LEDK
9	CLKN	29	LEDA
10	GND	30	NC
11	D0P	31	GND
12	D0N	32	NC(TP-RST)
13	GND	33	NC(TP-INT)
14	D3P	34	GND
15	D3N	35	NC(TP-SDA)
16	GND	36	NC(TP-SCL)
17	ID	37	GND
18	RESET	38	NC(TP-VDD)
19	TE	39	GND
20	GND		

NOTES:

1. DISPLAY TYPE: TFT, TRANSFLECTIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: AVDD=5.8V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING DIRECTION
4. DRIVER IC: ST7703I
5. OPERATING TEMP: -20°C ~ 70°C
6. STORAGE TEMP: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED If=40mA, Vf=22.4V(TYP.)
8. SURFACE LUMINANCE: 80CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: GLARE
10. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT



DETAIL A
(SCALE 100:1)



DETAIL B
(SCALE 2:1)



BACKLIGHT CIRCUIT DIAGRAM



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	TUFFY		MODULE P/N:
CHECKED BY:		SCALE : 1/1	MI0550ET-1
APPROVED BY:		UNIT: mm	DESCRIPTION:
VERSION NO:	01	SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	TUFFY	2025.11.17
VER.	REVISED DESCRIPTION	REVISER	DATE